



# Low Power Methodology Manual: For System-on-Chip Design (Integrated Circuits and Systems)

*David Flynn, Robert Aitken, Alan Gibbons, Kaijian Shi, Michael Keating*

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## **Low Power Methodology Manual: For System-on-Chip Design (Integrated Circuits and Systems)**

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This book provides a practical guide for engineers doing low power System-on-Chip (SoC) designs. It covers various aspects of low power design from architectural issues and design techniques to circuit design of power gating switches. In addition to providing a theoretical basis for these techniques, the book addresses the practical issues of implementing them in today's designs with today's tools.

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